



<b>10:00</b>	Cardiff Model+ Investigating Bias and Frequency Interpolation using RAPID Behavioural Model Extraction	Vince Mallette, <i>Focus Microwave Inc.</i>
<b>10:20</b>	Tomorrow's Wireless: How 5G and the IoT is Shaping the Future of Wireless Technology	David Hall, <i>National Instruments</i>
<b>10:40</b>	Noise Figure Measurements for Multiport Applications	Volker Herrmann, <i>Rohde &amp; Schwarz</i>
<b>11:00</b>	Next Generation Small Form Factor Radio	Wyatt Taylor, <i>Analog Devices, Inc.</i>
<b>11:20</b>	RF Sampling Transceiver Drives Wide Bandwidth Communication and DPD Systems	Russell Hoppenstein, <i>Texas Instruments</i>
<b>11:40</b>	Integrated PIN Diode Switches Offer Improved Performance with Lower DC Power	Ray Baker, <i>MACOM</i>
<b>12:00</b>	 The Future of RF Semiconductor Test	<b>Panelists:</b> Steve Reyes, VNA Product Manager, Anritsu; Jason White, Director of Marketing, RF & Wireless Test, National Instruments Chris Scholz, North American Product Manager for VNAs, R&S; Greg Peters, President and General Manager, Aerospace/Defense/ Government Solutions, Keysight; Brad Nelson, Director of Engineering RF Components and Sources, Qorvo; Rene Rodriguez, Senior Antenna Engineer, Skyworks <b>Moderator:</b> Pat Hindle, Editor, Microwave Journal
<b>13:00</b>	DAC Residual Phase Noise Measurements and Improvements Demonstrated Using Low Noise Regulators	Pete Delos, Jarrett Liner, <i>Analog Devices, Inc.</i>
<b>13:20</b>	A Novel Extended Drain NMOS Device for High Power Amplification in Commercial 300mm RFSOI Technology Platform	Luis ANDIA, <i>GLOBALFOUNDRIES</i>
<b>13:40</b>	WIPL-D Domain Decomposition Method: Fast and Accurate Simulation of Electrically Large Problems on Desktop PCs	Branko Mrdakovic, <i>WIPL-D</i>
<b>14:00</b>	The Impact of New Spectrum on Design and Test	Roger Nichols, Keynote <i>Keysight Technologies</i>
<b>14:40</b>	Tunable and Fixed Filtering Solutions Enhances Dynamic Range and Flexibility of 4G-LTE Measurements:	Rafi Hershtig, <i>K&amp;L Microwave Inc.</i>
<b>15:00</b>	Pushing Board-to-Board Interconnect Performance: Application-Specific Design	Don Bradfield, <i>Southwest Microwave, Inc.</i>
<b>15:20</b>	The Design of High Performance L-band GaN PAs Using Commercially Available Discrete Transistors	Andrew Dearn , Robert Smith, Stuart Glynn and Liam Devlin; Plextek RFI Ltd., and Raj Santhakumar, Richard Martin and Kim Tran; Qorvo Inc., <i>Plextek RFI</i>
<b>15:40</b>	Improving Bias Filtering Rejection in Printed Wiring Board Designs	Sunita Shah, <i>Cobham Microelectronic Solutions</i>
<b>16:00</b>	Highly Integrated Antenna Frontend Design for Radar, SATCOM and 5G	Winfried Simon, <i>IMST GmbH</i>
<b>16:20</b>	Using Very-Near-Field Scanners for Self-Interference Debugging in Communication Circuits	Dr. Arturo Mediano, Erkan T. Ickam, <i>University of Zaragoza, Spain+ EMSCAN</i>